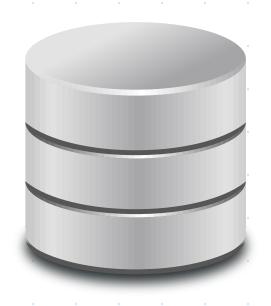
# CSE 350

## Advanced Data Structures

## Abstraction Mismatch



Permitt	2/p	State	 Type
	14228	NY	Electrical
163	14228	W Y	onstruction
125	10001	N Y	 = lectrical
1 2 7			



4 4 km d 4 km d

### **Assumptions**

- DB is one table
- We know schema
- We don't need to shore it

given Schena Provide RID -> Record (get) Record -> RID (record store a

Record Record

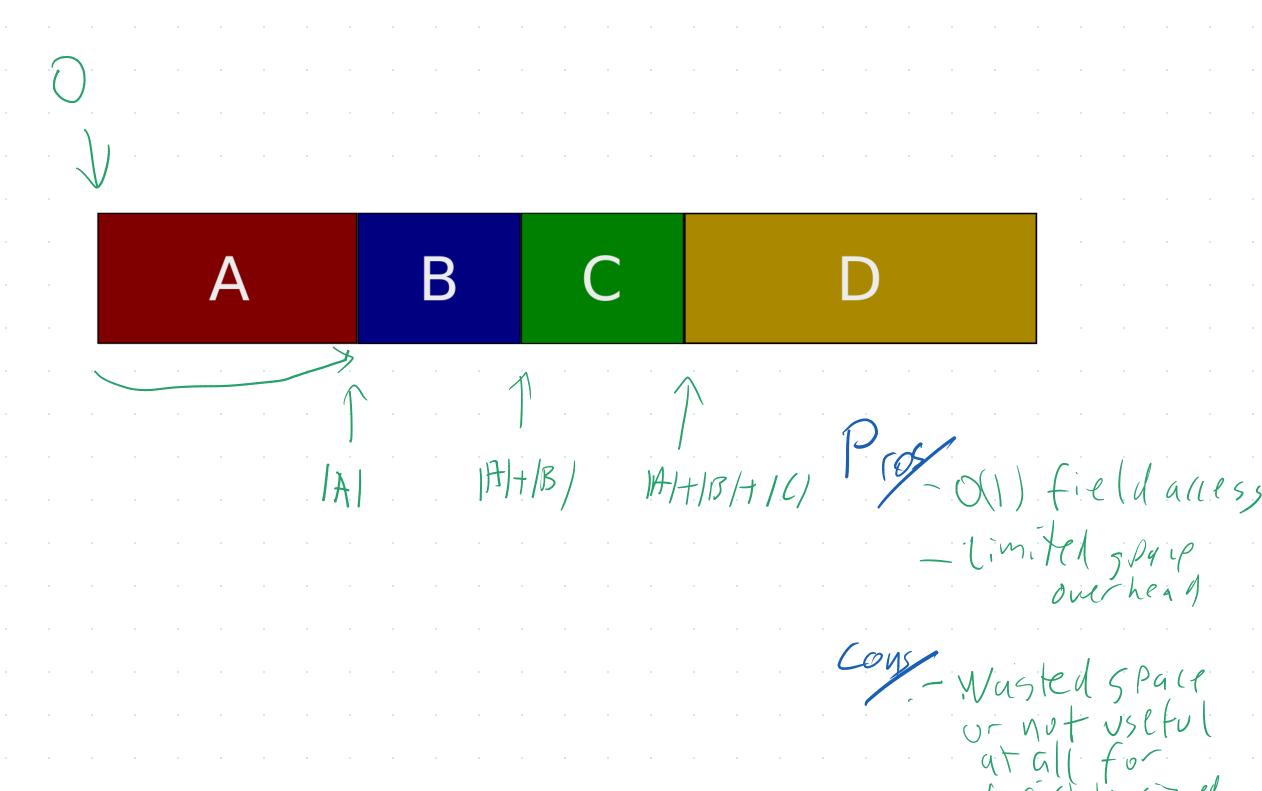
1, Record > Bits V 2, Bits R<sub>1</sub> Bits R<sub>2</sub> -> Pages

3. Finding Pages in a Table

# Challenge 1: Record to Bits

Flec = / Polombo = 3 (123, 14228, WT, T

#### Variation 1: Fixed Layout



Janiable Sizel

#### Variation 2: Delimiters



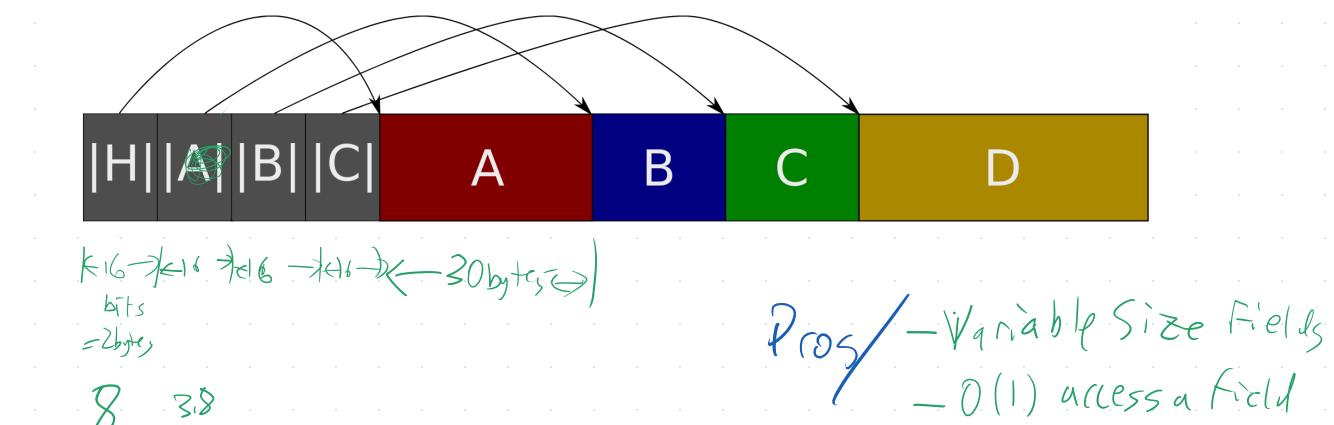
A	1,1	В	1,1	C	1,1	D

P107/- Juri ub le lengts fields

Cous/-symbol (ant appear indute -O([record])

CPV to access

#### Variation 3: Headers



### Challenge 2: Records to Pages

All layers are the same

Tricks: Same

Identity: RID instead of Field

Stray, not stret

Variable length

Fixed type

Variable types

#### Variation 1: Fixed

1 R/ 5 0	Mmmm
	anno and a
21/2/	unn.
3	mm m
	in the second of
	0000000000000
n n n 6	
7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	
n Mety	1121100000
	Which Slots are
	rel pare by clearing in use
	1 (174/19.40) {

What is a RID

Index of Slot

Where can i ald

tre next record

Store in use bits

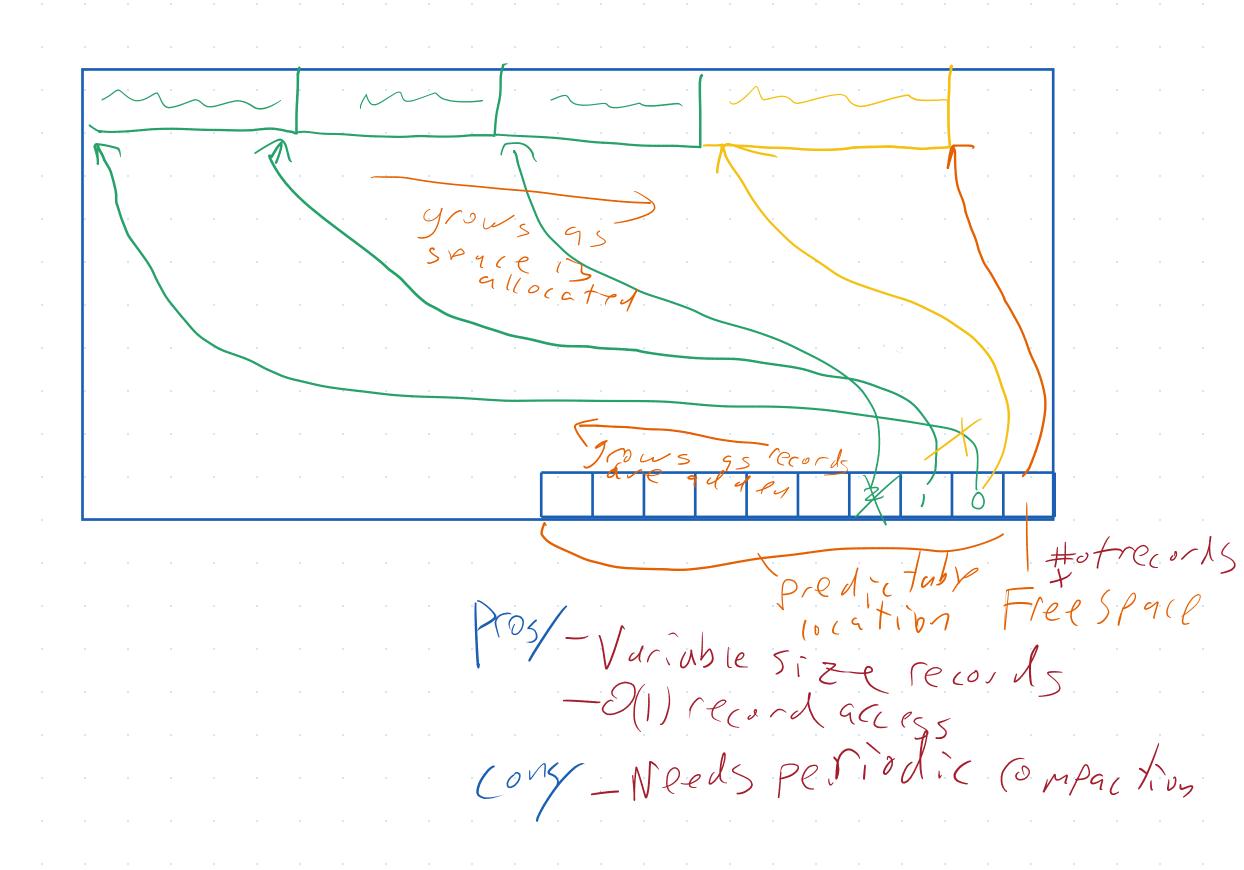
Pron/-somewhat fast allocation -fast retrieve (1)

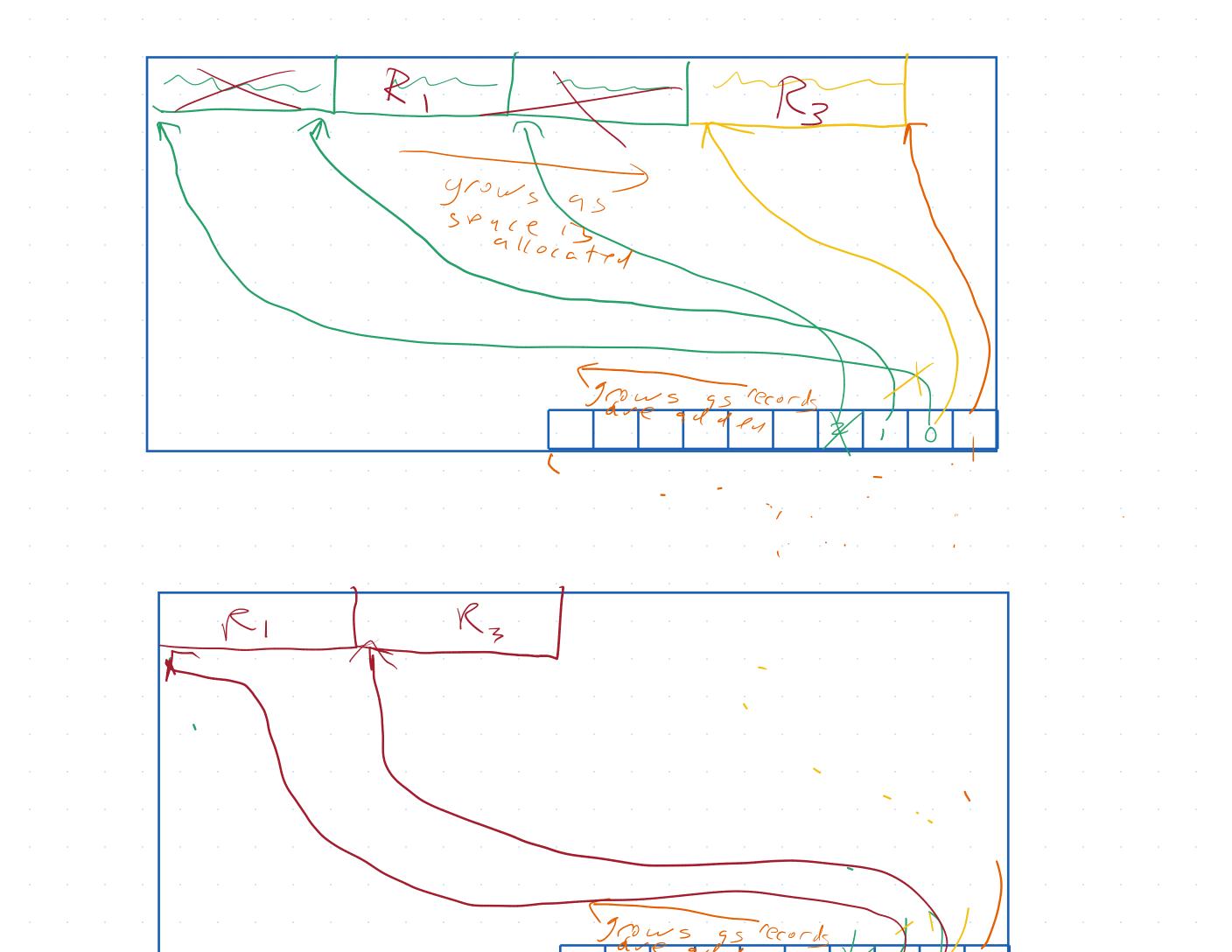
Cons/-Stuck W/ fixed size Records

<u>Variation</u>	1b: Fixed (Packel) and a second	
	basic idea	
	$M_{1}$	
	mmm R	
	MMMM R3 Delete Ry	
	mm f	
	more last record into its spot	
	Pros/Records to	oye the
	-Fusterallo	Latio
9		
n Meta	17 Cons/-Array 11/1	2x Modons
	Stable	

# Variation 2: Delimited

#### Variation 3: Footers

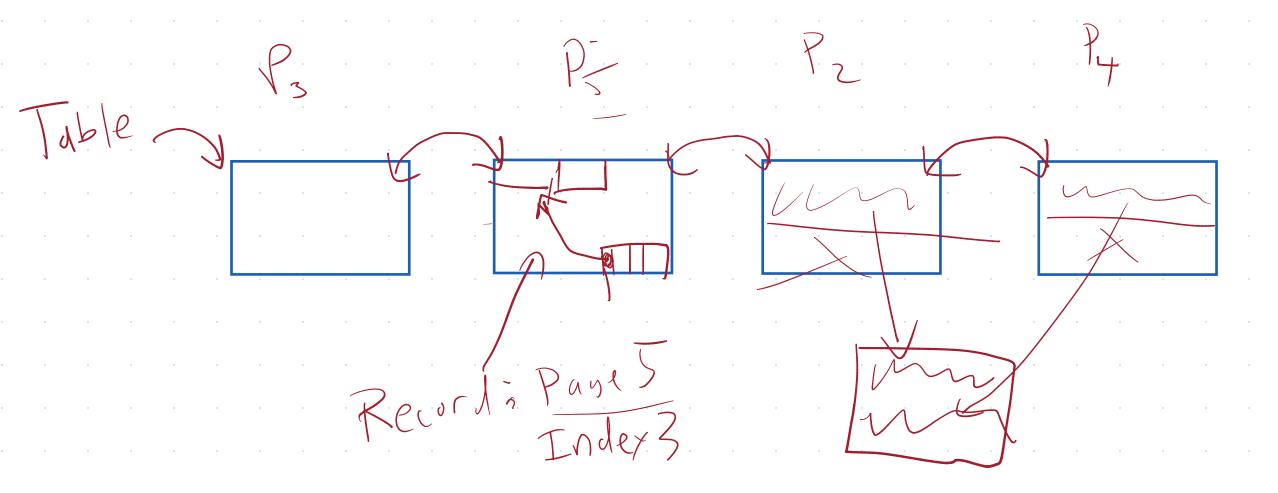




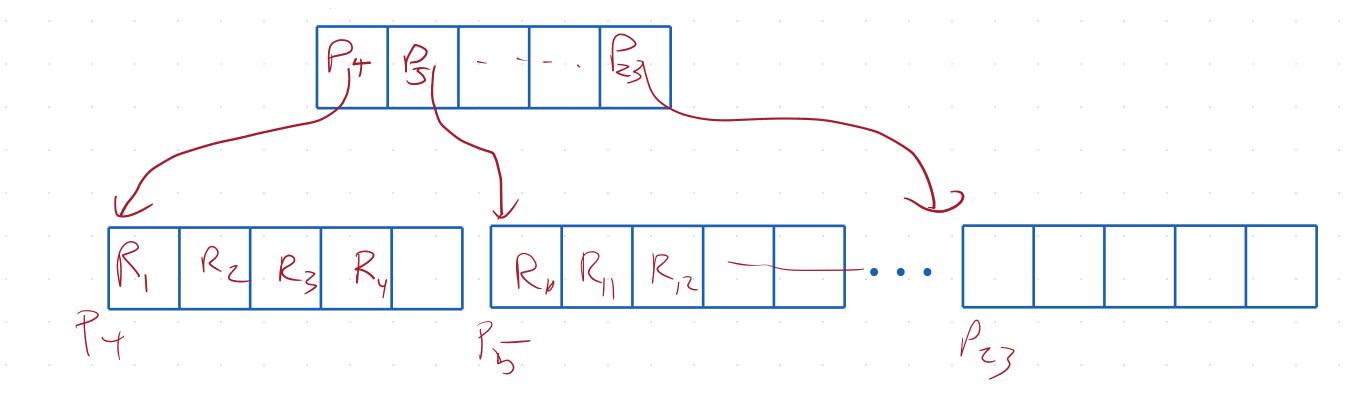
Predictuby Free Space

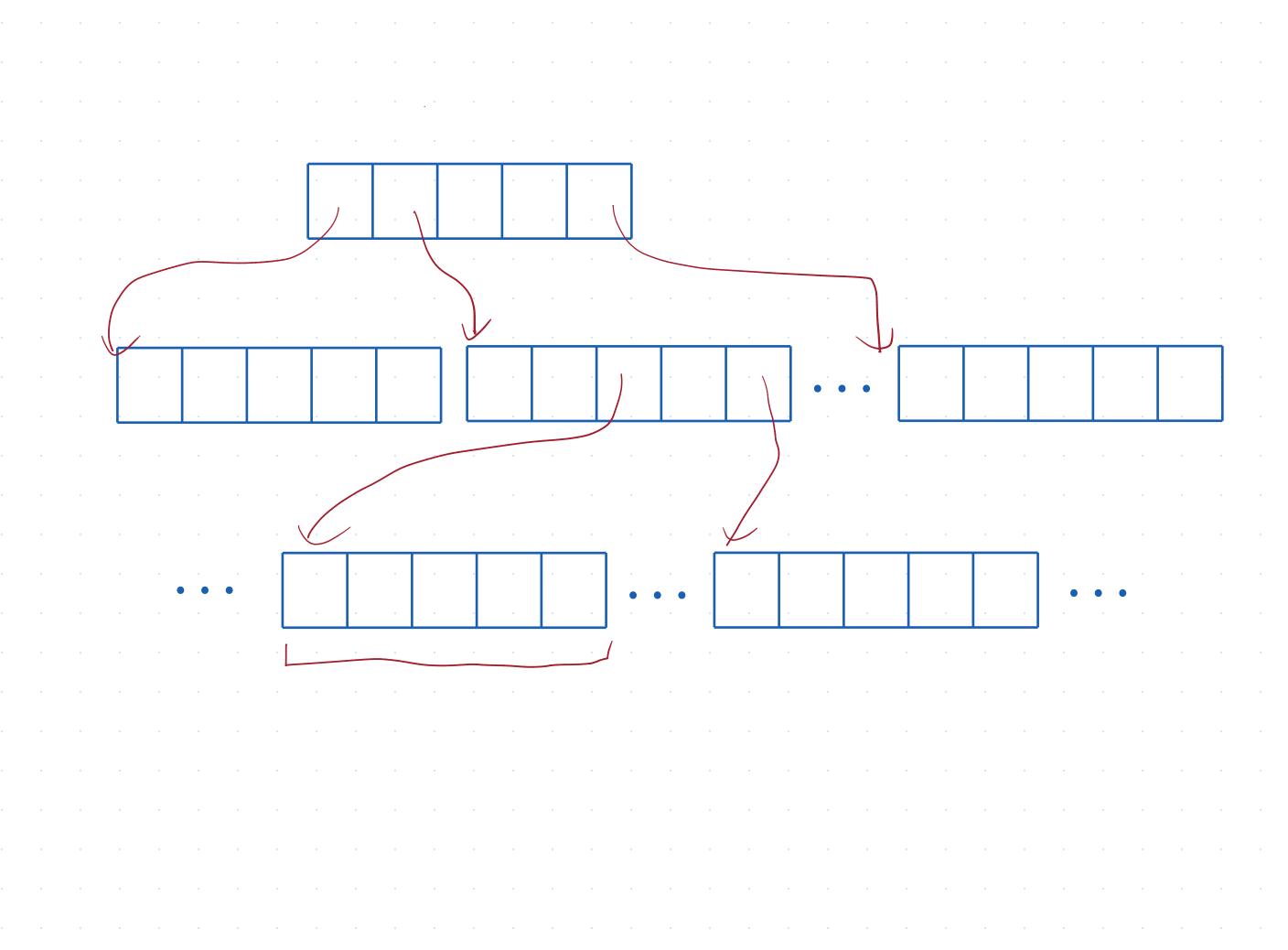
# Challenge 3: Page Assignment

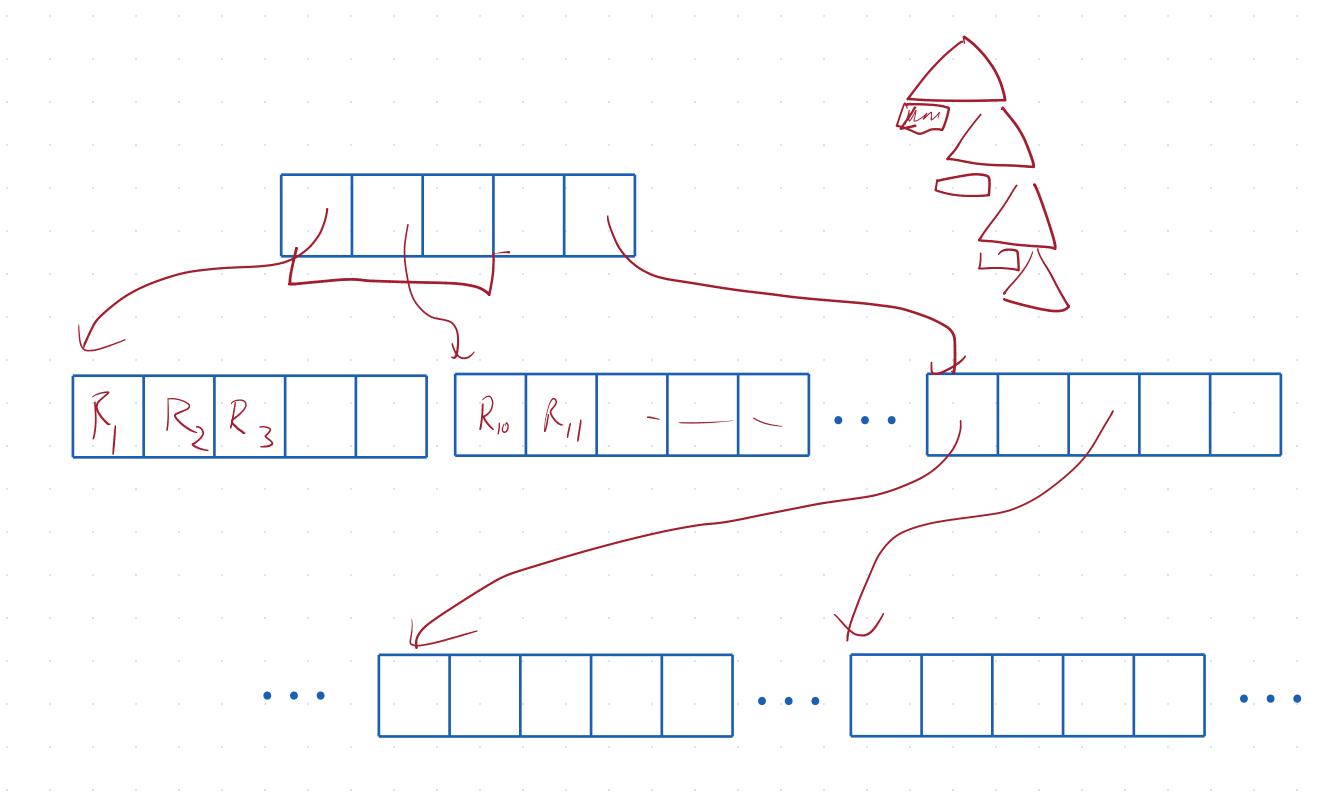
## Variation 1: Linked List

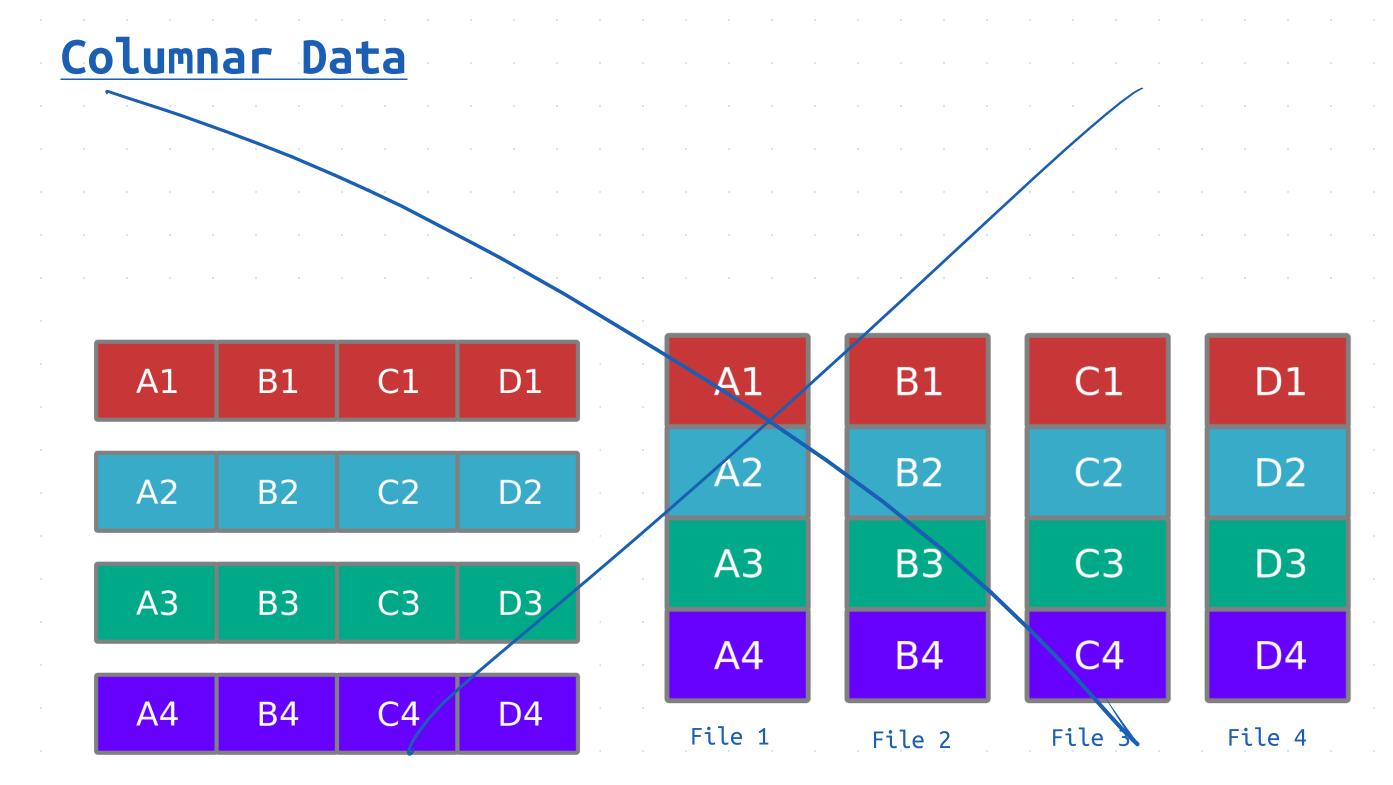


# Variation 2: Inode









File 1

Skipped a familia